

Faculty Vitae

Name of the Faculty					
Dr.V.Jayalakshmi					
Education					
	Degree	Discipline	Institution	Year	
UG	B.E	EEE	J.J.College of Engineering and Technology.	2000	
PG	M.Tech	Power System	National Institute of Technology (N.I.T), Tiruchirappalli	2006	
PhD	Ph.D.	EEE	Bharath University	2017	
Academic experience					
Institution	Title	From	To	FT/PT	
Bharath University	Professor	July 2007	Till Date	FT	
Prince Shri Venkateshwara Engineering college	Assistant Professor	June 2006	June 2007	FT	
M.A.M College of Engineering	Lecturer	June 2001	May 2006	FT	
Certifications or professional registrations					
Nil					
Membership in professional organizations					
M.I.S.T.E					
Honors and awards					
Nil					
Service activities (within and outside of the institution)					
Nil					
List of Publications					
<ol style="list-style-type: none"> 1. Jayalakshmi.V and Dr.N.O.Gunasekhar, "Implementation of Discrete PWM Control Scheme on Dynamic Voltage Restorer for the Mitigation of Voltage Sag /Swell". 2013 International Conference on Energy Efficient Technologies for Sustainability.pp-1036-1040. 2. Jayalakshmi.V and Dr.N.O.Gunasekhar, "Dynamic Voltage Restorer using Three Phase AC-AC Converter" Journal of Theoretical and Applied Information Technology 10th June 2014. Vol. 64 No.1 pp.142-147. 					

3. Jayalakshmi.V and Dr.N.O.Gunasekhar , “Mitigation of Voltage Sag by Dynamic Voltage Restorer using Single Phase Z Source Inverter” International Journal of Applied Engineering Research, Volume 9, Number 17 (2014) pp. 4181-4188
4. Jayalakshmi.V, “Wireless Sensor Network for Performance Monitoring of Electrical Machine” Middle-East Journal of Scientific Research 20 (8): pp.996-999, 2014.
5. Jayalakshmi.V, “A Method to Reduce Neutral Current in Three Phase Four Wire Electric Distribution Systems by Using Active Power Filter” Middle-East Journal of Scientific Research 20 (11): pp.1561-1564, 2014
6. Jayalakshmi.V, “Variable Speed Cage Machine Wind Generation Unit “Middle-East Journal of Scientific Research 20 (11): pp.1670-1676, 2014
7. Jayalakshmi.V, “Real and Reactive Power Coordination for a Unified Power Flow Controller” Middle-East Journal of Scientific Research 20 (11): pp.1680-1685, 2014
8. V Jayalakshmi, M.Kalavathy, “Cascaded Seven Levels H-Bridge Inverter Control of DSTATCOM for Compensation of Reactive Power and Harmonics” International Journal of Science and Research (IJSR), January 2013Vol 1, Issue1, pp 596-600.
9. V.Jayalakshmi Ms. K. Anusuya, “Efficiency Enhancement of Leakage Current Reduction in Three Phase Transformer less Solar Systems” IOSR Journal of Electrical and Electronics Engineering (IOSR-JEEE) Dec 2013 Volume 3, Issue 5 pp. 46-55.
10. V. Jayalakshmi, Jafar Ali and S.P.Vijayaragavan, “ Simulation of an Interline Dynamic Voltage Restoring and Displacement Factor Controlling Device(IVDFC)” International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 4, Issue 3, March 2015 pp.1285-1293.

Recent professional development activities

- PG Part Time Coordinator, Bharath University
- Placement Coordinator for EEE Department, Bharath University.
- Board of Studies Coordinator for EEE Department, Bharath University.